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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of)
Hisashi OHTANI et al.)
Serial No. 10/026,802)
Filed: December 27, 2001)
For: METHOD FOR)
MANUFACTURING A)
SEMICONDUCTOR DEVICE)

Art Unit: 2811
Examiner: W. Mintel

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with The United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on 5-6-03
Adele M. Stamper
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RESPONSE TO ELECTION REQUIREMENT

Honorable Commissioner of Patents
P.O. Box 1450,
Alexandria, VA 22313-1450

Sir:

In response to the election requirement in the Office Action of April 7, 2003, Applicants hereby elect without traverse Embodiment 3 having a structure as depicted in Figures 6A-6E. As described in the specification, Embodiment 3 is directed to an active matrix device having a thin film transistor, which has a crystalline semiconductor film formed in accordance with the present invention, that is, the crystalline semiconductor film as recited in the pending claims.

In that claims 1-20 are directed to a thin film transistor having a specific crystal orientation that is not unique to any of the species set forth in the Official Action, it is respectfully submitted that claims 1-20 read on the elected Embodiment 3. Claims 1-20 are also believed to be generic to Embodiments 1-6 set forth in the Official Action.

Examination on the merits is requested.

Respectfully submitted,

Eric J. Robinson
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